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42390P13736

**PATENT** 

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:	)
Glew, et al for Intel Corporation	Examiner: Pyzocha, Michael J.
Serial No.: 10/039,961	) Art Unit: 2137
Filing Date: December 31, 2001	<b>)</b>
For: PROCESSOR SUPPORTING EXECUTION OF AN AUTHENTICATED CODE INSTRUCTION	) ) ) )

## CERTIFICATE OF MAILING/TRANSMISSION

I hereby certify that this correspondence is being facsimile transmitted to the United States Patent and Trademark Office on the date indicated below and that this paper has been addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, fax number (571) 273-8300.

Date of Deposit: September 13, 2010

Name of Person Transmitting Correspondence:

Signature Date

Mail Stop Appeal Brief Patents Commissioner for Patents PO Box 1450 Alexandria, VA 22313-1450

## REPLY BRIEF

It is respectfully argued that a prima facie case of obviousness has not been presented due to the reasons set forth in the appeal brief. In addition, the combination of the references would not result in the claimed invention. Combining England and Okada as suggested by the examiner would result in hiding the execution of curtained code (based on England) in a system that includes a processor that additionally has a function

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to prevent the illegal execution of a program (based on Okada). The combination would not result in a processor that decodes and executes a launch instruction, wherein the execution of the launch instruction meets the requirements of the claims of the prevent application.

Therefore, each of the final rejections should be reversed and the claims subject to this Appeal should be allowed to issue. Please charge any necessary fees, including extension fees, to our Deposit Account No. 50-0221.

Respectfully submitted,

Date: September 13, 2010

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